

Hardware Emulation and FPGA Prototyping for Embedded Systems Design

GUEST SPEAKER

Dr. Lauro Rizzatti

GM and Marketing Vice President, EVE-USA, Inc.

When: **13th December 2010, 3.00 p.m. to 4.00 p.m.**

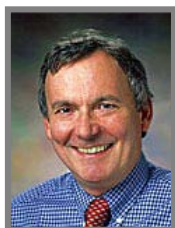
Where: **Institute of Microelectronics, Singapore**

11 Science Park Road Singapore Science Park II Singapore 117685

Abstract

The escalating increase in hardware complexity and software contents makes Embedded System verification a daunting task that is defeating software-based verification tools. Only hardware-assisted verification can cope with the need to process tens or hundreds of billions of verification cycles and trace design bugs across the boundary of the SOC hardware and its embedded software. In this technical seminar, we will cover best in class techniques and tools to achieve early prototyping and establish consistent verification methodologies across all levels of abstraction, from behavioural level down to gate level and from transaction level down to cycle accurate simulation.

About the Speaker



Dr. Lauro Rizzatti has more than 30 years of experience in the EDA and Test Equipment industry, where he held responsibilities in top management, product marketing, technical marketing, and engineering across the United States, Europe, Japan, and PacRim. Prior to joining EVE, Dr. Rizzatti held various positions in companies such as Get2Chip, Synopsys, Mentor Graphics, Teradyne, Alcatel, and Italtel. Dr. Rizzatti has published several articles and technical papers in the trade press and has presented at numerous international technical conferences. Dr. Rizzatti holds a PhD in Electronic Engineering from the Università degli Studi di Trieste, Italy.

About EVE

EVE is the worldwide leader in hardware/software co-verification solutions, including hardware description language (HDL) acceleration and extremely fast emulation, with installations at nine of the top 10 semiconductor companies. EVE offers the most verification cycles per dollar in the industry, at about two orders of magnitudes (100X) more than the closest competitor. EVE's ZeBu product line offers automatic compilation, fast co-simulation with HDL/C/C++/SC/SV testbenches and excellent hardware debugging capabilities. ZeBu is used to thoroughly debug SOC hardware and to accelerate embedded software development ahead of silicon availability, and ultimately to shorten time to tapeout and improve design quality.

Registration

Pre-registration is required. Closing date is 9th December 2010. To register, please log on:

http://easar.eventsHub.sg/ems_wb_Details.aspx?CallID=28&EventID=148967

Location Map

